



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Blandy**

Serial No.: **09/671,875**

Filed: **September 28, 2000**

For: **Apparatus and Method for  
Creating Instruction Bundles in an  
Explicitly Parallel Architecture**

§  
§  
§  
§  
§  
§  
§  
§  
§

Group Art Unit: **2183**

Examiner: **Tsai, Henry**

Attorney Docket No.: **AUS9-2000-0587-US1**

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. 1.97

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**RECEIVED**  
**APR 02 2004**  
**Technology Center 2100**

Applicants request that the information listed on the attached Form PTO-1449 be considered by the Office during the pendency of the above entitled application, pursuant to 37 C.F.R. 1.97.

Please charge any fees necessary for prosecution of the present application to Deposit Account No. 09-0447. If any extension of time is required, such extension is hereby requested. Please charge any additional required fee for extension of time to Deposit Account No. 50-0392.

In accordance with 37 C.F.R. 1.97(h), the filing of this Information Disclosure Statement shall not constitute an admission that any information cited therein is, or is considered to be, material to patentability as defined in 37 C.F.R. 1.56(b). In the interest of full and complete disclosure to the Office, some or all of the art cited herein may not be considered by Applicant(s) or the Undersigned to be material under the new standards of materiality defined in 37 C.F.R. 1.56(b), enacted March 16, 1992, but may be material under the old standard of materiality defined in 37 C.F.R. 1.56(a), last amended on November 28, 1988, or may merely be technical background which

04/01/2004 ANDHDAF1 00000001 09671875

01 FC:1006

180.00 DP

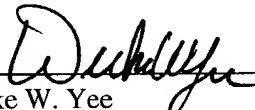
Blandy - 09/671,875  
Page 1 of 2

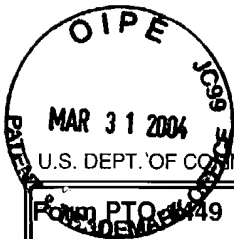
may be of interest to the Examiner. In accordance with 37 C.F.R. 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made.

This Information Disclosure Statement is being filed before the mailing date of a final action or a notice of allowance. A check for the required fee of \$180.00 as set forth in § 1.17(p) is enclosed.

Respectfully submitted,

Date: 3/29/04

  
Duke W. Yee  
*Registration No. 34,285*  
CARSTENS, YEE & CAHOON, LLP  
P.O. Box 802334  
Dallas, Texas 75380  
(972) 367-2001



<b>LIST OF PRIOR ART CITED BY APPLICANT</b> <i>(Use several sheets if necessary)</i>	ATTORNEY DOCKET NO. <b>AUS9-2000-0587-US1</b>	SERIAL NO. <b>09/671,875</b>
	<b>RECEIVED</b> <b>APR 02 2004</b> <b>Technology Center 2100</b>	
	APPLICANT <b>Blandy</b>	GROUP ART UNIT <b>2183</b>
FILING DATE <b>September 28, 2000</b>		

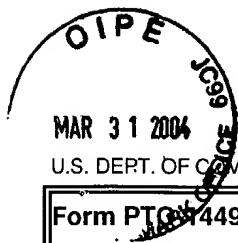
**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE
	AA 6,640,313 B1	Oct. 28, 2003	Quach	714/10	Dec. 21, 1999
	AB 6,539,473 B1	Mar. 25, 2003	Hubacher et al.	713/2	Sep. 2, 1999
	AC 6,487,601 B1	Nov. 26, 2002	Hubacher et al.	709/229	Sep. 30, 1999
	AD 6,466,972 B1	Oct. 15, 2002	Paul et al.	709/222	Mar. 31, 1999
	AE 6,240,510 B1	May 29, 2001	Yeh et al.	712/236	Aug. 6, 1998
	AF 5,504,932	Apr. 2, 1996	Vassiliadis et al.	395/800	Jun. 7, 1995
	AG 5,448,746	Sep. 5, 1995	Eickemeyer et al.	395/800	Jan. 25, 1994
	AH 6,571,385 B1	May 27, 2003	Muthukumar et al.	717/150	Mar. 22, 1999
	AI 6,412,105 B1	Jun. 25, 2002	Maslennikov et al.	717/9	Dec. 24, 1998
	AJ 6,640,315 B1	Oct. 28, 2003	Hwu et al.	714/17	Jun. 26, 1999
	AK 6,615,403 B1	Sep. 2, 2003	Muthukumar et al.	717/160	Jun. 30, 2000
	AL 6,041,399	Mar. 21, 2000	Terada et al.	712/24	Jun. 27, 1997
	AM 6,622,238 B1	Sep. 16, 2003	Benjamin et al.	712/226	Jan. 24, 2000
	AN 6,513,109 B1	Jan. 28, 2003	Gschwind et al.	712/200	Aug. 31, 1999
	AO 6,301,705 B1	Oct. 9, 2001	Doshi et al.	717/9	Oct. 1, 1998
	AP 5,778,219	Jul. 7, 1998	Amerson et al.	395/591	Feb. 7, 1996
	AQ 5,768,510	Jun. 16, 1998	Gish	395/200.33	Jul. 1, 1996
	AR 5,748,936	May 5, 1998	Karp et al.	395/394	May 30, 1996
	AS 5,634,023	May 27, 1997	Adler et al.	395/591	Jul. 1, 1994
	AT 5,628,016	May 6, 1997	Kukol	395/704	Jun. 15, 1994
	AU 5,627,981	May 6, 1997	Adler et al.	395/582	Jul. 1, 1994
	AV 6,151,703	Nov. 21, 2000	Crelier	717/5	May 20, 1996
	AW 6,385,660 B2	May 7, 2002	Griesemer et al.	709/315	Oct. 6, 1997

DATE CONSIDERED

EXAMINER

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



MAR 31 2004

U.S. DEPT. OF COMMERCE PATENT AND TRADEMARK OFFICE

Page 2 of 3

Form PTO-449

**LIST OF PRIOR ART CITED  
BY APPLICANT**

*(Use several sheets if necessary)*

ATTORNEY DOCKET NO.  
**AUS9-2000-0587-US1**

SERIAL NO.  
**09/671,875**

APPLICANT **Blandy**

FILING DATE **September 28, 2000**

GROUP ART UNIT **2183**

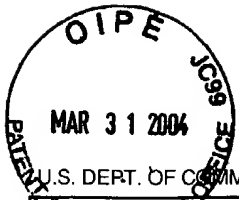
**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE
	AX 6,622,234 B1	Sep. 16, 2003	Pechanek et al.	712/22	Jun. 21, 2000
	AY 6,560,624 B1	May 6, 2003	Otani et al.	708/655	Jan. 3, 2000
	AZ 6,446,195 B1	Sep. 3, 2002	Ganapathy et al.	712/221	Jan. 31, 2000
	BA 6,442,676 B1	Aug. 27, 2002	Guenthner	712/210	Jun. 30, 1999
	BB 6,321,322 B1	Nov. 20, 2001	Pechanek et al.	712/24	Apr. 5, 2000
	BC 6,286,346 B1	Sep. 11, 2001	Hocken, Jr. et al.	70/260	Apr. 30, 1998
	BD 6,101,592	Aug. 8, 2000	Pechanek et al.	712/20	Dec. 18, 1998
	BE 6,061,781	May 9, 2000	Jain et al.	712/221	Jul. 1, 1998
	BF 5,574,928	Nov. 12, 1996	White et al.	395/800	Apr. 26, 1994
	BG 5,502,827	Mar. 26, 1996	Yoshida	395/375	May 12, 1995
	BH 5,848,246	Dec. 8, 1998	Gish	395/200.58	Jul. 1, 1996
	BI 6,158,048	Dec. 5, 2000	Lueh et al.	717/9	May 29, 1998
	BJ 6,567,974 B1	May 20, 2003	Czajkowski	717/151	Feb. 25, 2000
	BK 6,317,872 B1	Nov. 13, 2001	Gee et al.	717/7	Apr. 6, 1998
	BL 6,336,213 B1	Jan. 1, 2002	Beadle et al.	717/5	Dec. 3, 1998
	BM 6,339,841 B1	Jan. 15, 2002	Merrick et al.	717/10	Apr. 16, 1999
	BN 6,324,687 B1	Nov. 27, 2001	Beadle et al.	717/6	Dec. 3, 1998
	BO 6,604,167 B1	Aug. 5, 2003	Blandy et al.	711/100	Sep. 28, 2000
	BP 6,311,325 B1	Oct. 30, 2001	Levine et al.	717/4	Oct. 22, 1998
	BQ 6,018,799	Jan. 25, 2000	Wallace et al.	712/300	Jul. 22, 1998
	BR 6,035,120	Mar. 7, 2000	Ravichandran	395/705	May 28, 1997
	BS 6,108,771	Aug. 22, 2000	Gaertner et al.	712/217	Jan. 29, 1998
	BT 4,435,753	Mar. 6, 1984	Rizzi	364/200	Nov. 3, 1980
	BU 4,642,764	Feb. 10, 1987	Auslander et al.	364/300	Aug. 13, 1984

DATE CONSIDERED

EXAMINER

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



<b>Form PTO-1449</b>  <b>LIST OF PRIOR ART CITED BY APPLICANT</b> <i>(Use several sheets if necessary)</i>	ATTORNEY DOCKET NO. <b>AUS9-2000-0587-US1</b>	SERIAL NO. <b>09/671,875</b>
	<b>RECEIVED</b>	
	APPLICANT <b>Blandy</b>	<b>APR 02 2004</b>
	FILING DATE <b>September 28, 2000</b>	<b>Technology Center 2100</b>

**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE
	BV 4,782,444	Nov. 1, 1988	Munshi et al.	364/300	Dec. 17, 1985
	BW 4,961,141	Oct. 2, 1990	Hopkins et al.	364/200	Dec. 16, 1988
	BX 5,613,121	Mar. 18, 1997	Blainey	395/709	Oct. 19, 1994
	BY 5,828,886	Oct. 27, 1998	Hayashi	395/709	Feb. 23, 1995
	BZ 5,966,537	Oct. 12, 1999	Ravichandran	395/709	May 28, 1997
	CA 6,110,226	Aug. 29, 2000	Bothner	717/7	Feb. 19, 1998
	CB 6,317,876 B1	Nov. 13, 2001	Kunz et al.	717/9	Jun. 8, 1999
	CC 6,408,433 B1	Jun. 18, 2002	Click, Jr. et al.	717/154	Apr. 23, 1999

**OTHER PRIOR ART** *(including author, title, date, pertinent page, etc.)*

	CD	Fitzgerald et al., "Marmot: An Optimizing Compiler for Java", Microsoft Research, Jun. 16, 1999, Technical Report MSR-TR-99-33, pp. 1-29.
	CE	Azevedo et al., "Java Annotation-Aware Just-In-Time (AJIT) Compilation System", ACM 1999, San Francisco, CA, USA, pp. 142-151.
	CF	Larus et al., "SPIM S20: A MIPS R2000 Simulator", Computer Sciences Department, University of Wisconsin, Madison, copyright 1990-1997, pp. 1-25.
	CG	Aho et al., "Compilers: Principles, Techniques, and Tools", copyright 1986, Bell Telephone Labs, Inc., 4 pages.
	CH	Sinclair et al., "ASIC Design for Conditional Nested Loops with Predicate Registers", 08/11/1999, Circuits and Systems, 1999, 42 <sup>nd</sup> Midwest Symposium, vol. 2, pgs. 874-877.
	CI	"The Java Virtual Machine Specification", Sun Microsystems, Inc., copyright 1997, <a href="http://java.sun.com/docs/books/vmspec/html/Instructions2.doc14.html">http://java.sun.com/docs/books/vmspec/html/Instructions2.doc14.html</a> .
	CJ	Fernandez, "Simple and Effective Link-Time Optimization of Modula-3 Programs", 11-7-1994, pp. 1-7.
	CK	Ishizaki et al., "Design, Implementation, and Evaluation of Optimizations in a Just-In-Time Compiler, ACM, 6/1999, pp. 119-122.

DATE CONSIDERED	EXAMINER
-----------------	----------

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.